

S/N 08/903,486

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Leonard Forbes et al.  
Serial No.: 08/903,486  
Filed: July 29, 1997  
Title: SILICON CARBIDE GATE TRANSISTOR

Examiner: W. Mintel  
Group Art Unit: 2811  
Docket: 303.326US1

# 25/IDS  
5/1/01  
V. Vassell

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants further request that a copy of the 1449 form, initialed by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Applicants have included the fee of \$180.00 under 37 C.F.R. §§ 1.97(c) and 1.17(p). Please charge any additional fees or credit any overpayment to Account No. 19-0743.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.



Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

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Date 23 April 2001

By \_\_\_\_\_

Robert E. Mates  
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**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 23rd day of April, 2001.

Name Amy Moriarty

Signature

Amy Moriarty

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